

FIG. 1

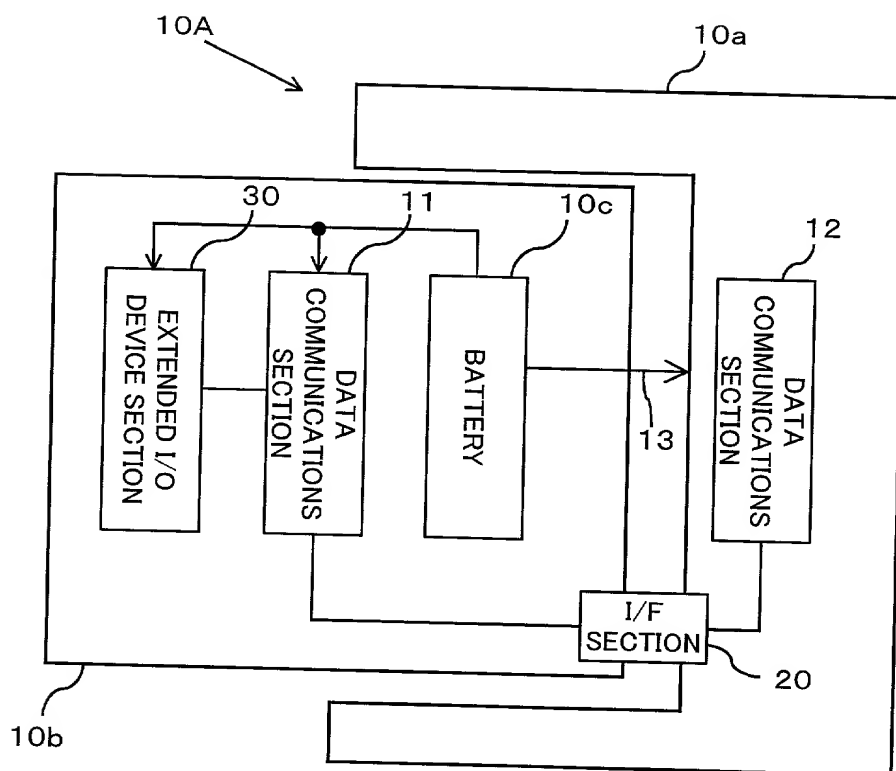


FIG. 2

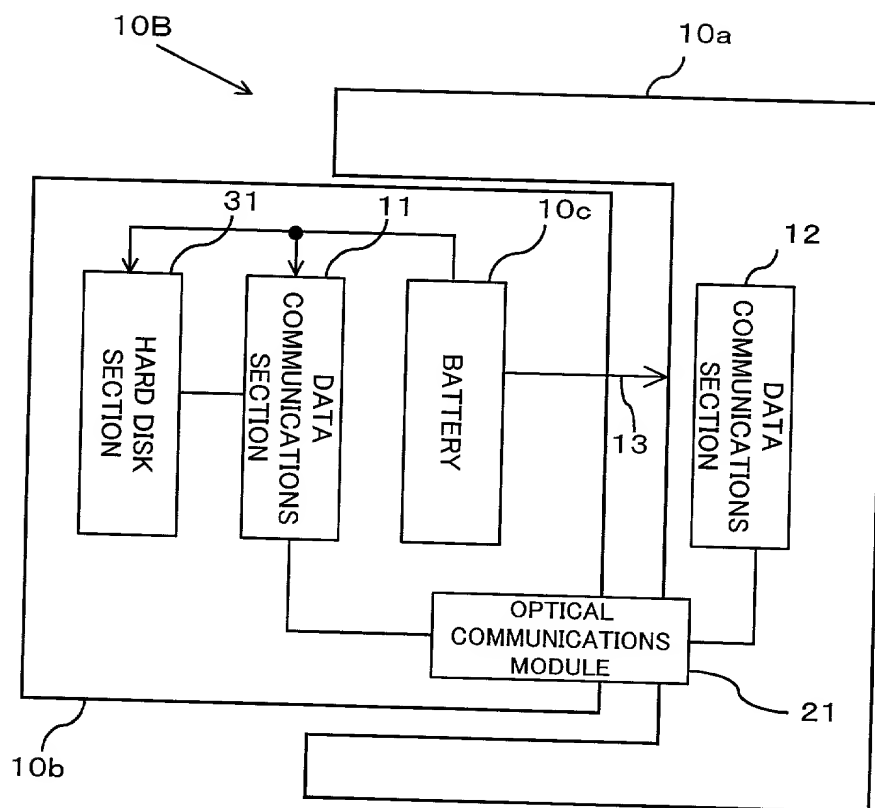


FIG. 3

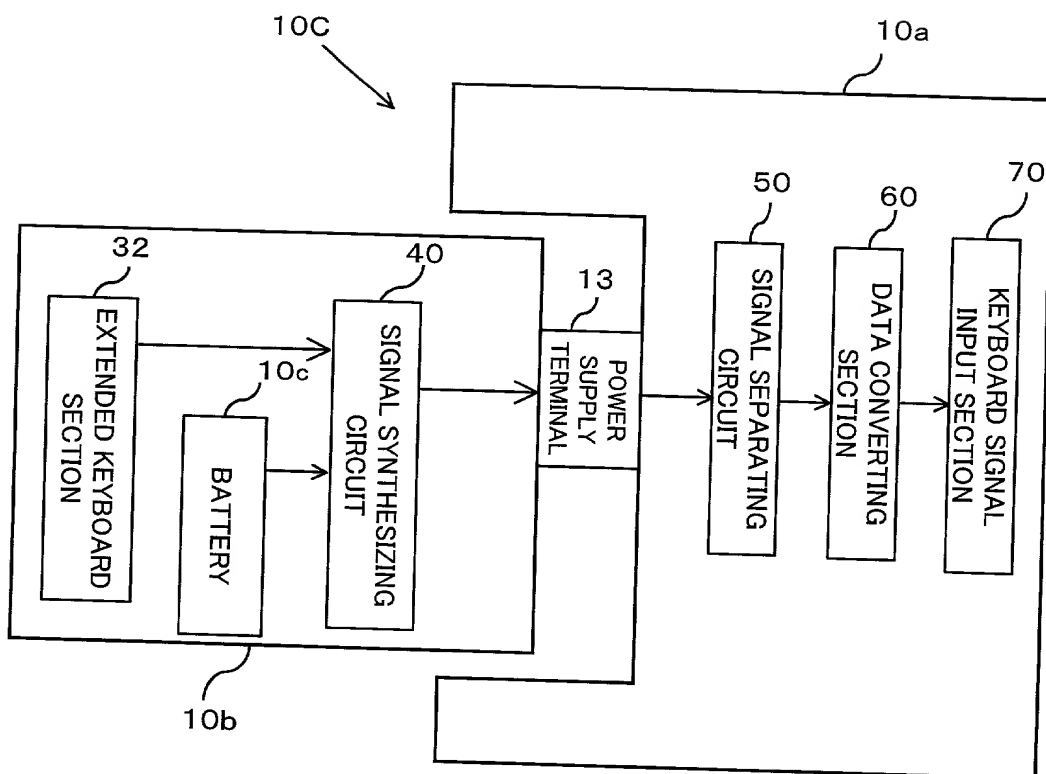


FIG. 4 is a schematic diagram of a circuit for synthesizing a signal from a keyboard section.

FIG. 4

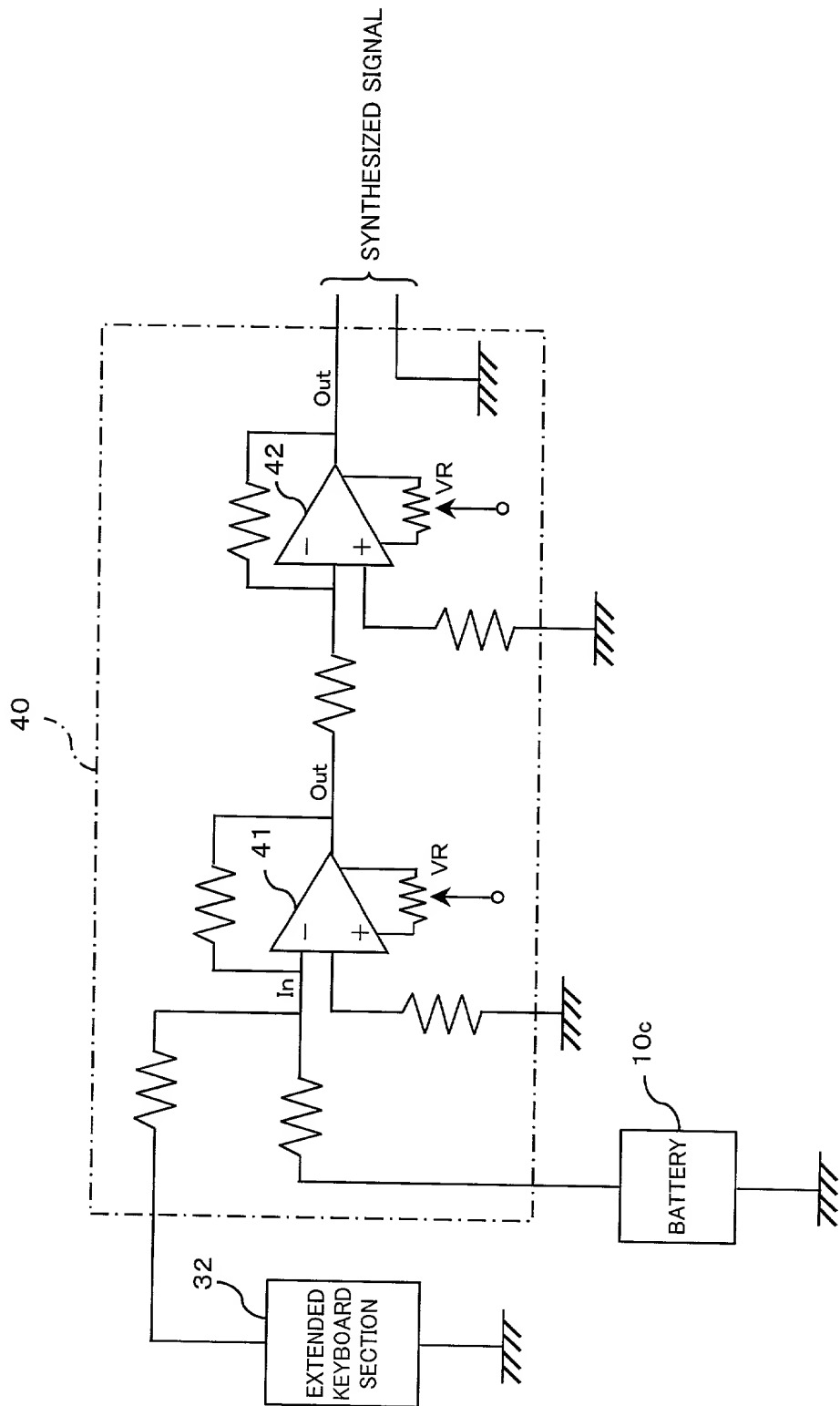


FIG. 5 is a schematic diagram of a circuit 50, which includes two operational amplifiers 51 and 52, a battery 10c, a synthesized signal source, and various resistors and ground connections.

FIG. 5

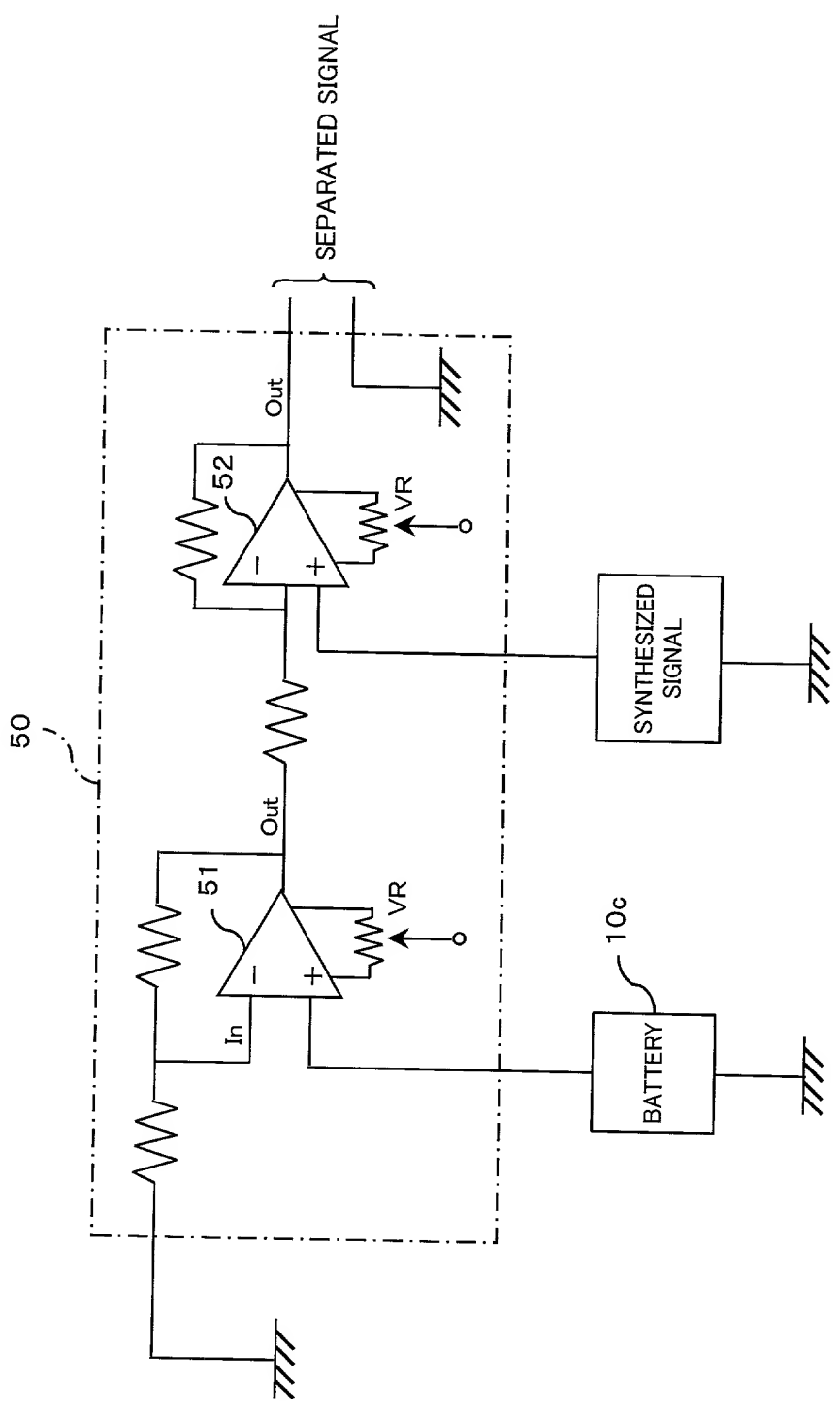


FIG. 6

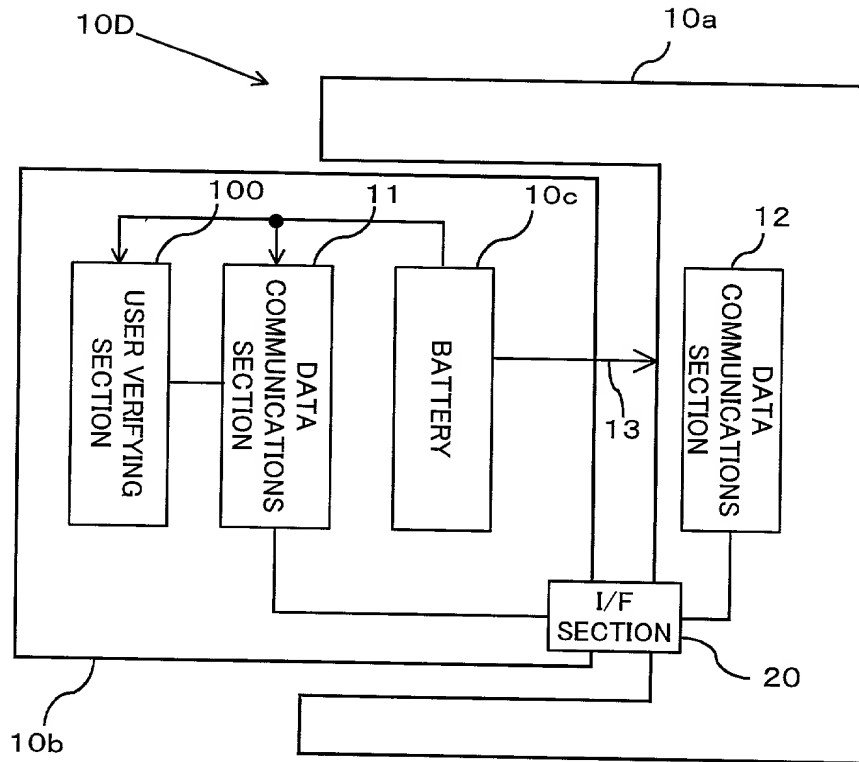


FIG. 7

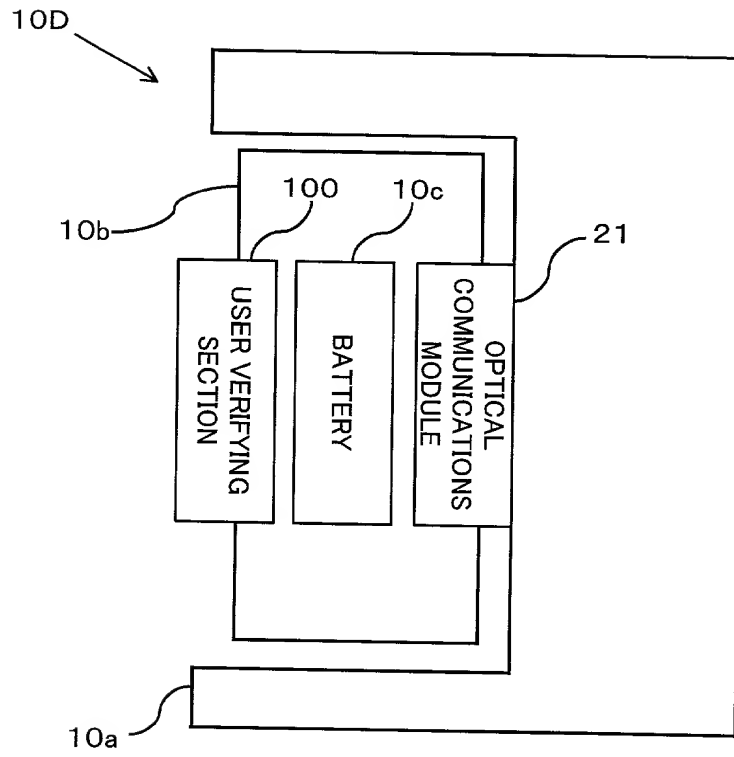
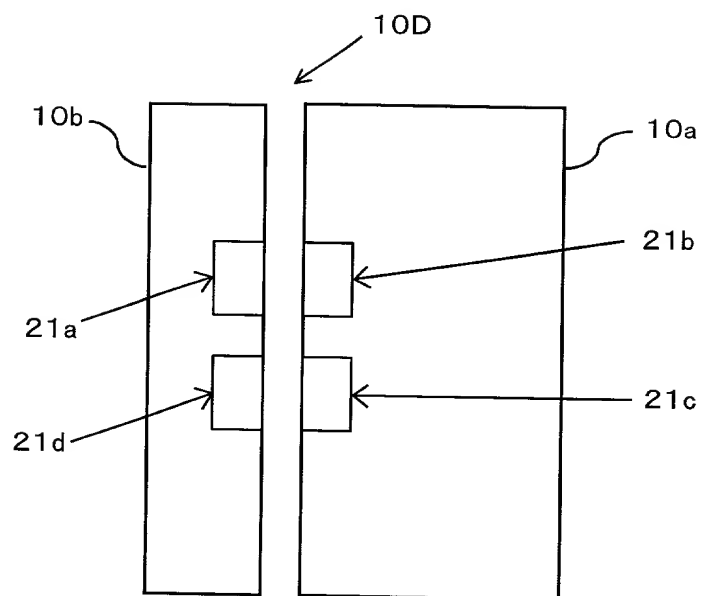


FIG. 8





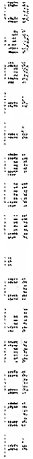
[illegible]

FIG. 10

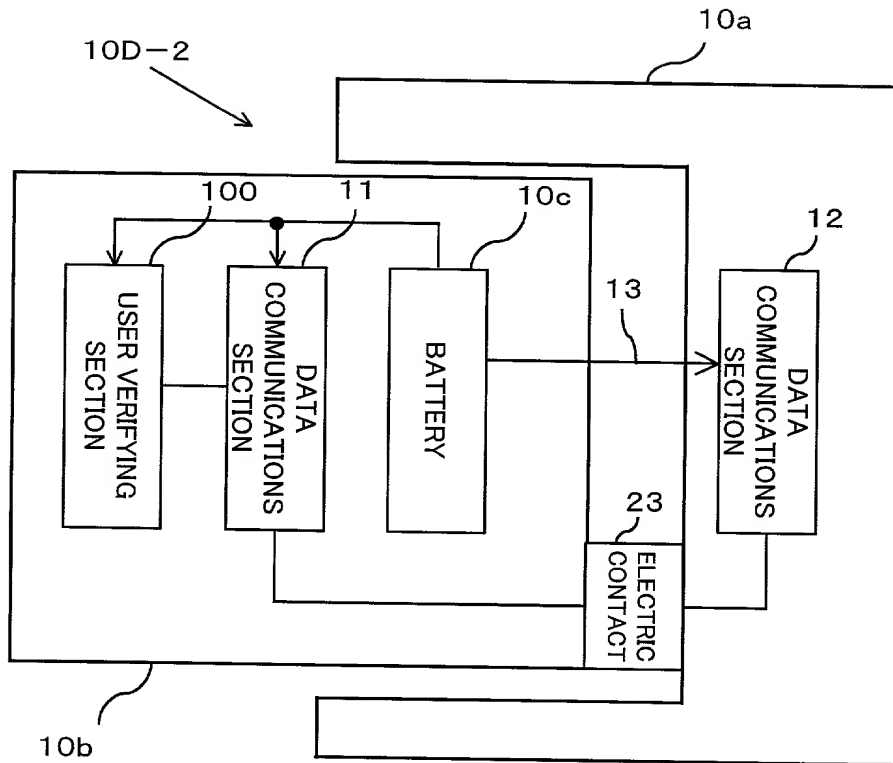


FIG. 11

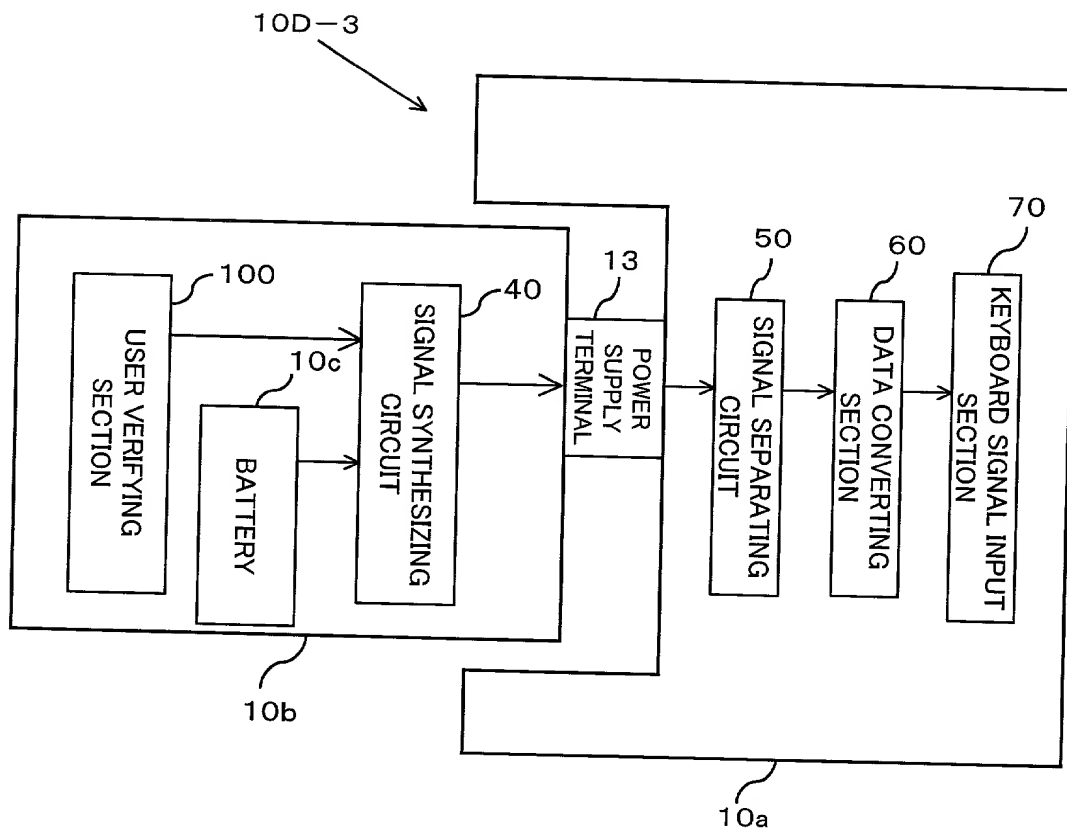


FIG. 12

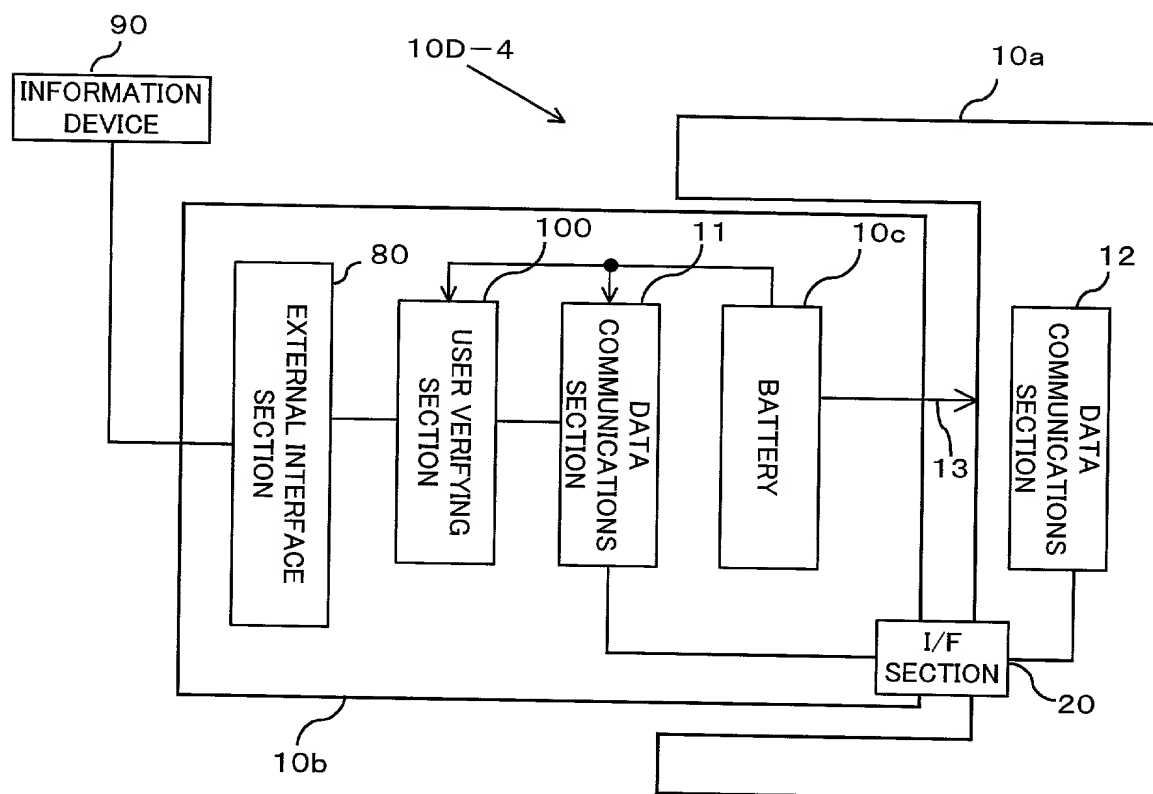


FIG. 13

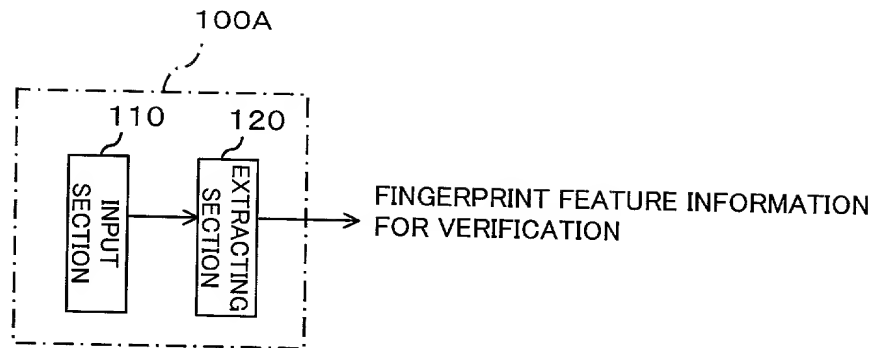


FIG. 14

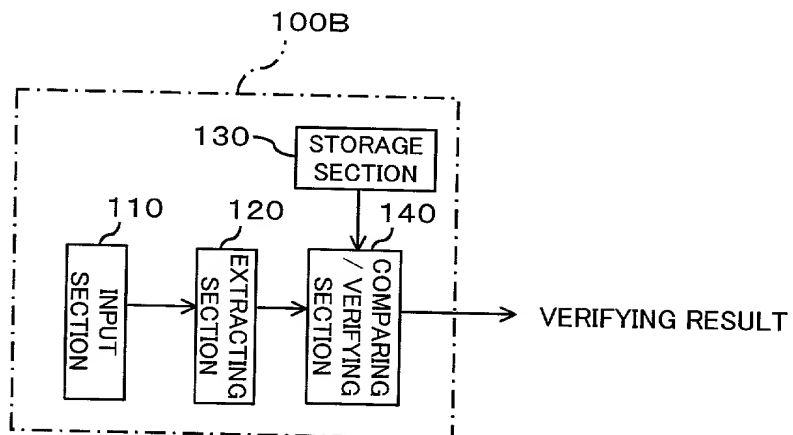


FIG. 15

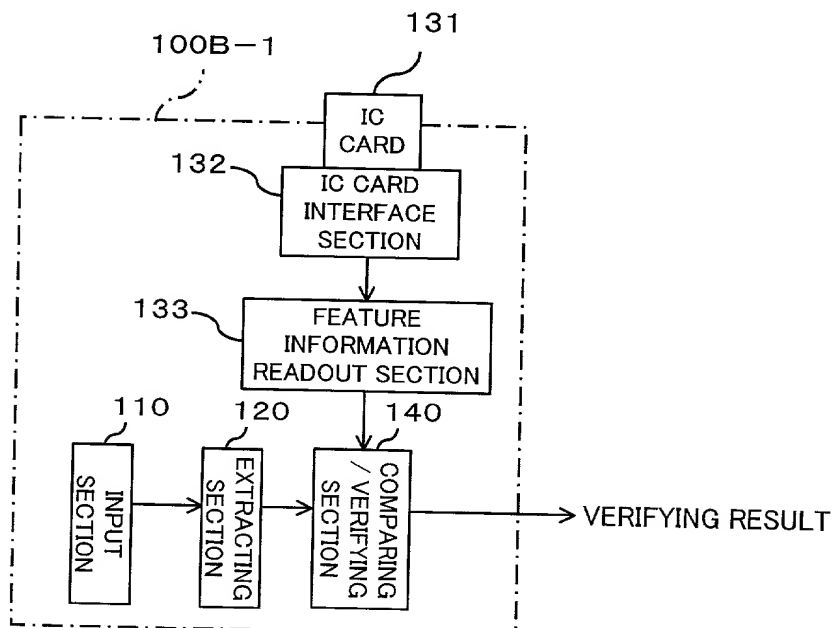


FIG. 16

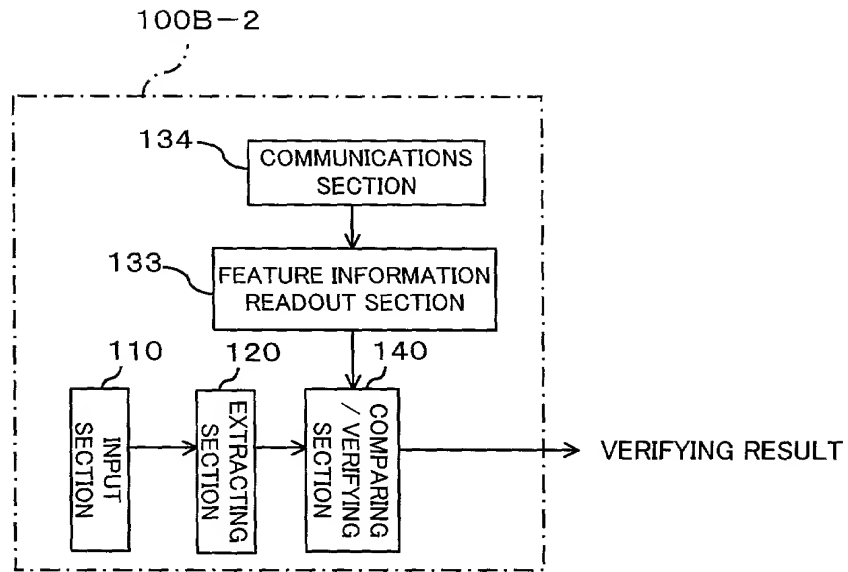


FIG. 17

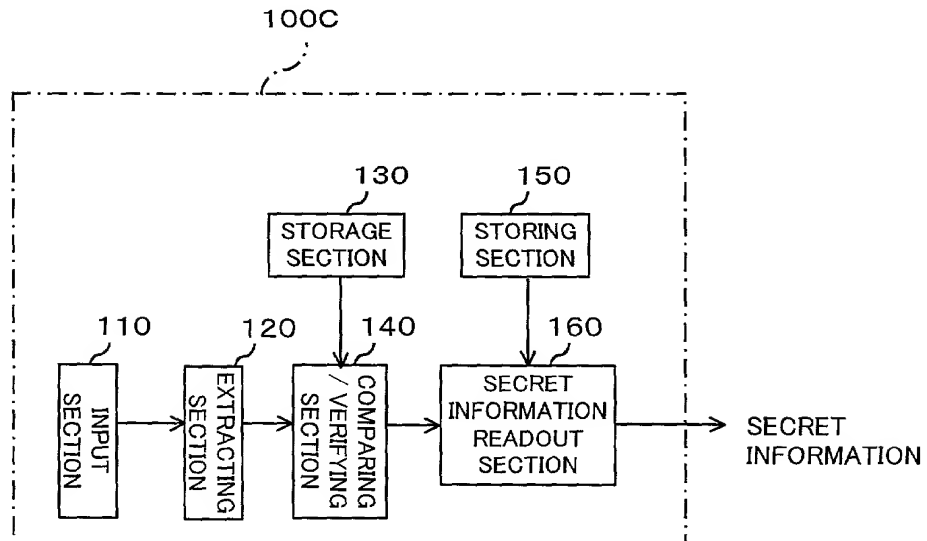


FIG. 18 is a block diagram of a cryptographic device 10D-5, showing its internal components and their interconnections.

FIG. 18

